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Applicant: Chambers

Art Unit: 2829

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Serial No.: 10/649,425

Examiner: Sarkar, A.

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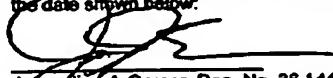
Title: STRUCTURE AND METHOD TO FABRICATE SELF-ALIGNED
TRANSISTORS WITH DUAL WORK FUNCTION METAL GATE ELECTRODESREQUEST FOR EXTENSION OF TIME

Commissioner of Patents
PO Box 1450
Alexandria, VA 22313-1450

Dear Sir:

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office at 703-572-9306 on the date shown below:



Jacqueline J. Garner, Reg. No. 36,144

April 20, 2005
Date

Pursuant to 37 CFR 1.138(a), Applicant(s) respectfully petitions the Commissioner for an extension of the shortened statutory period for response in the above-identified Application.

The fee for this extension is indicated below:

- One Month (\$120)
- Two Months (\$450)
- Three Months (\$1,020)
- Four Months (\$1,590)

Please charge the fee to deposit account no. 20-0668. Any further necessary extension of time is hereby requested. Charge any and all fees to deposit account no. 20-0668.

Respectfully Submitted,


Jacqueline J. Garner
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